



MULTI-INNO TECHNOLOGY CO., LTD.

LCD MODULE SPECIFICATION

Model : MI10065A-G

Revision	
Engineering	
Date	
Our Reference	

MODE OF DISPLAY**Display mode**

- STN : ☐ Yellow green
☐ Grey
☐ Blue (negative)
☐ FSTN positive
☐ FSTN negative

Display condition

- ☐ Reflective type
☐ Transflective type
☐ Transmissive type
☐ Others

Viewing direction

- ☐ 6 O' clock
☐ 12 O' clock
☐ 3 O' clock
☐ 9 O' clock

LCD MODULE NUMBER NOTATION:

GENERAL DESCRIPTION

Display mode : 100 x 65 dots, Graphic COG LCD module
 Interface : Serial
 Driving method : 1/65 duty, 1/9 bias
 Controller IC : Samsung KS0724 or equivalent
 For the detailed information, please refer to the IC specifications.

MECHANICAL DIMENSIONS

Item	Dimension	Unit	Item	Dimension	Unit
Outline Dimension			Viewing Area	28.0(L)x26.7(W)	mm
No Backlight (N)	32.5(L)x37.0(W)x2.0(Max)(H)	mm	Dot Pitch	0.26(L)x0.38(W)	mm
LED Sided Backlight(L)	32.5(L)x37.0(W)x6.0(Max)(H)	mm	Dot Size	0.24(L)x0.36(W)	mm

CONNECTOR PIN ASSIGNMENT

Pin No.	Symbol	Function
1	VØ	LCD Operation Voltage
2	V4	LCD Operation Voltage
3	V3	LCD Operation Voltage
4	V2	LCD Operation Voltage
5	V1	LCD Operation Voltage
6	C2-	Voltage Converter
7	C2+	Voltage Converter
8	C1+	Voltage Converter
9	C1-	Voltage Converter
10	C3+	Voltage Converter
11	VOP	Operating Voltage for LCD (IC Pin: Vout)
12	VSS	Power Supply (0V)
13	VDD	Power Supply for Logic
14	SID	Serial Data
15	SCLK	Serial Clock
16	RS	Register Select
17	RESETB	Reset
18	CS1B	Chip Select
*19	A	Backlight Supply Terminal (+)
*20	K	Backlight Supply Terminal (-)

Note (*): Pin 19, 20 are for side-lited LED backlight versions only

[illegible]

BLOCK DIAGRAM



PIN NUMBER	SYMBOL	I/O	FUNCTION
1	V0	—	LCD Operating Voltage
2	V4	—	LCD Operating Voltage
3	V3	—	LCD Operating Voltage
4	V2	—	LCD Operating Voltage
5	V1	—	LCD Operating Voltage
6	C2—	—	Voltage converter
7	C2+	—	Voltage converter
8	C1+	—	Voltage converter
9	C1—	—	Voltage converter
10	C3+	—	Voltage converter
11	Vop	—	Operating voltage for LCD(IC pin:Vout)
12	VSS	—	Power Supply (0V)
13	VDD	—	Power Supply for Logic
14	SID	I/O	Serial data
15	SCLK	I	Serial clock
16	RS	I	Register select
17	RESETB	I	Reset
18	CS1B	I	Chip select
19	A	—	Backlight Supply Terminal (+) *
20	K	—	Backlight Supply Terminal (—) *

TOLERANCE IF NOT SPECIFY ±0.5mm		SCALE N.T.S:	UNIT IN mm		MULTI-INNO TECHNOLOGY CO., LTD		SHEET 2 OF 3	
REV.	REVISION RECORD		DATE	CUSTOMER	APPROVED	MODEL NO.	MI10065A-G	
00	1st ISSUE		21-10-02	STANDARD		TITLE:	BLOCK DIAGRAM	
01	Change heatseal version		29-10-02					
02	Add Backlight & Dimension		8-03-04		AGENT	APPROVED	DRAWN BY:	Hermus DATE : 8-03-04
						CHECKED BY :	DATE : 9/3/04	
						APPROVED BY :	DATE : 10/3/04	
				CUSTOMER REF.	OUR REF.			

ELECTRICAL CHARACTERISTICS

Conditions: VSS=0V, @Ta=25

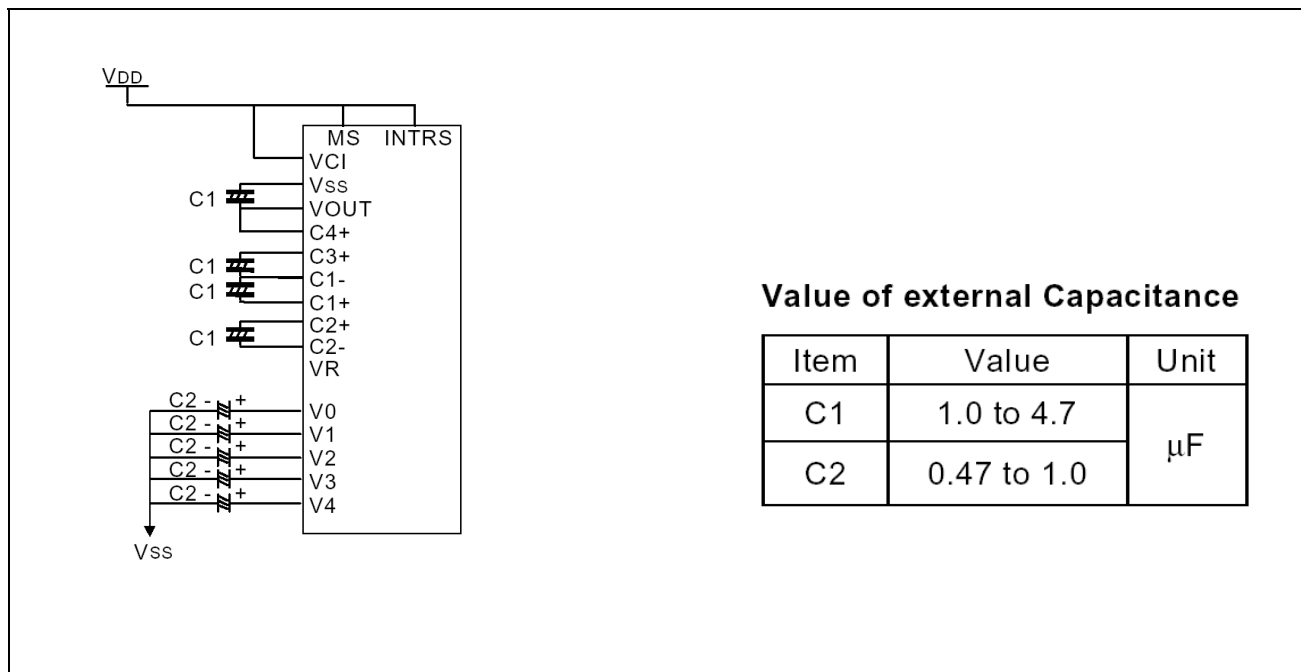
Item	Symbol	MIN.	TYP.	MAX.	Unit	Item	Symbol	MIN.	TYP.	MAX.	Unit
Supply Voltage	VDD	3.05	3.30	3.55	V	“H”Level Input Voltage	VIH	0.8 VDD	-	VDD	V
Supply Current	IDD	-	150	-	μA	“L”Level Input Voltage	VIL	0	-	0.2 VDD	V
Operating voltage for LCD	VOP	-	12V (*)	-	V	-	-	-	-	-	-
EL Backlight Voltage (VEL)						Backlight Current					
EL (@ Frequency 400Hz)	-	-	-	-	-	-	-	-	-	-	-
Side-lited LED Backlight Forward Voltage (VF)						Side-lited LED Backlight Forward Current (IF)					
White (Current @40mA)	VBL	-	3.2	3.5	V	White	IBL	-	40	50	mA
Blue	VBL	-	-	-	V	Blue	IBL	-	-	-	mA
Yellow Green	VBL	-	-	-	V	Yellow Green	IBL	-	-	-	mA

Note (*): Please refer to Connection Example (4X Boosting Circuit)

ABSOLUTE MAXIMUM RATINGS

Please make sure not to exceed the following maximum rating values under the worst application conditions

Item	Symbol	Rating (for normal temperature)	Rating (for wide temperature)	Unit
Supply Voltage	VDD	-0.3 to 7.0	-0.3 to 7.0	V
Input Voltage	VT	-0.3 to VDD+0.3	-0.3 to VDD+0.3	V
Operating Temperature	Topr	0 to 50	-20 to 70	
Storage Temperature	Tstg	-10 to 60	-30 to 80	

CONNECTION EXAMPLE**4X Boosting Circuit**

Note : C4+ and VOUT pin is internal shorted.(Module Pin Out VOP).

INSTRUCTION TABLE

×: Don't care

Instruction	RS	RW	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Description
Display ON / OFF	0	0	1	0	1	0	1	1	1	DON	Turn on/off LCD panel When DON = 0: display OFF When DON = 1: display ON
Initial display line	0	0	0	1	ST5	ST4	ST3	ST2	ST1	ST0	Specify DDRAM line for COM0
Set page address	0	0	1	0	1	1	P3	P2	P1	P0	Set page address
Set column address MSB	0	0	0	0	0	1	Y7	Y6	Y5	Y4	Set column address MSB
Set column address LSB	0	0	0	0	0	0	Y3	Y2	Y1	Y0	Set column address LSB
Read status	0	1	BUSY	ADC	ONOFF	RESETB	0	0	0	0	Read the internal status
Write display data	1	0	Write data								Write data into DDRAM
Read display data	1	1	Read data								Read data from DDRAM
ADC select	0	0	1	0	1	0	0	0	0	ADC	Select SEG output direction When ADC = 0: normal direction (SEG0→SEG131) When ADC = 1: reverse direction (SEG131→SEG0)
Reverse display ON / OFF	0	0	1	0	1	0	0	1	1	REV	Select normal / reverse display When REV = 0: normal display When REV = 1: reverse display
Entire display ON / OFF	0	0	1	0	1	0	0	1	0	EON	Select normal/entire display ON When EON = 0: normal display. When EON = 1: entire display ON
LCD bias select	0	0	1	0	1	0	0	0	1	BIAS	Select LCD bias
Set modify-read	0	0	1	1	1	0	0	0	0	0	Set modify-read mode
Reset modify-read	0	0	1	1	1	0	1	1	1	0	release modify-read mode
Reset	0	0	1	1	1	0	0	0	1	0	Initialize the internal functions
SHL select	0	0	1	1	0	0	SHL	×	×	×	Select COM output direction When SHL = 0: normal direction (COM0→COM63) When SHL = 1: reverse direction (COM63→COM0)
Power control	0	0	0	0	1	0	1	VC	VR	VF	Control power circuit operation
Regulator resistor select	0	0	0	0	1	0	0	R2	R1	R0	Select internal resistance ratio of the regulator resistor
Set reference voltage mode	0	0	1	0	0	0	0	0	0	1	Set reference voltage mode
Set reference voltage register	0	0	×	×	SV5	SV4	SV3	SV2	SV1	SV0	Set reference voltage register
Set static indicator mode	0	0	1	0	1	0	1	1	0	SM	Set static indicator mode
Set static indicator register	0	0	×	×	×	×	×	×	S1	S0	Set static indicator register
Power save	-	-	-	-	-	-	-	-	-	-	Compound Instruction of display OFF and entire display ON
NOP	0	0	1	1	1	0	0	0	1	1	<u>Non-Operation command</u>
Test Instruction_1	0	0	1	1	1	1	×	×	×	×	<u>Don't use this instruction</u>
Test Instruction_2	0	0	1	0	0	1	×	×	×	×	<u>Don't use this instruction</u>

RECOMMENDED INITIAL SETTINGS

Initial Display Line : 40H

LCD Bias Select : A2H

Power Control : 2FH

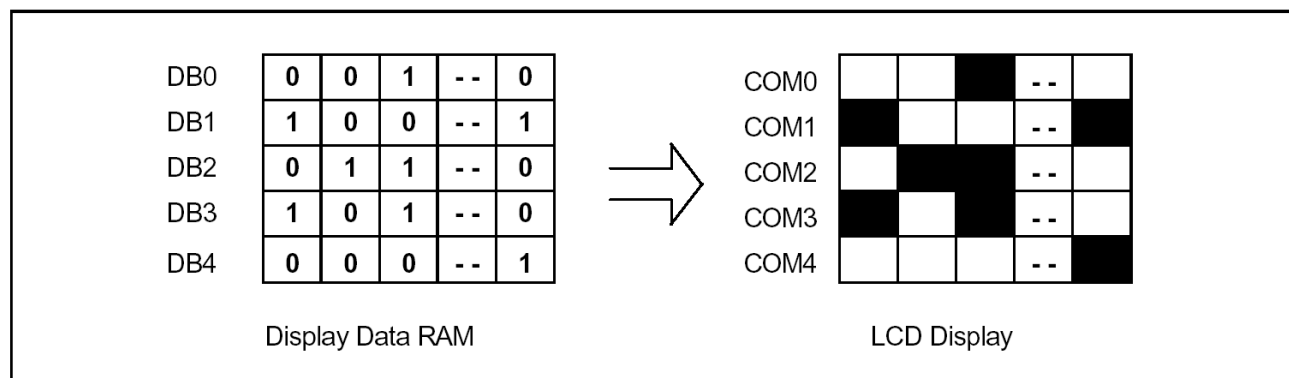
Regulator Resistor Select : 26H

Set Reference Voltage Register : 36H

SHL Select : C8H

DISPLAY DATA RAM (DDRAM)

The Display Data RAM stores pixel data for the LCD. It is 65-row by 132-column addressable array. Each pixel can be selected when the page and column addresses are specified. The 65 rows are divided into 8 pages of 8 lines and the 9th page with a single line (DB0 only). Data is read from or written to the 8 lines of each page directly through DB0 to DB7. The display data of DB0 to DB7 from the microprocessor correspond to the LCD common lines as shown in figure 6. The microprocessor can read from and write to RAM through the I/O buffer. Since the LCD controller operates independently, data can be written into RAM at the same time as data is being displayed without causing the LCD flicker.



RAM-to-LCD Data Transfer

PAGE ADDRESS CIRCUIT

This circuit is for providing a Page Address to DISPLAY-DATA-RAM shown in figure 8. It incorporates 4-bit Page Address register changed by only the "Set Page" instruction. Page Address 8 (DB3 is "H", but DB2, DB1 and DB0 are "L") is a special RAM area for the icons and display data DB0 is only valid. When Page Address is above 8, it is impossible to access to on-chip RAM.

LINE ADDRESS CIRCUIT

This circuit assigns DDRAM a Line Address corresponding to the first line (COM0) of the display. Therefore, by setting line address repeatedly, it is possible to realize the screen scrolling and page switching without changing the contents of on-chip RAM as shown in figure 8. It incorporates 6-bit line address register changed by only the initial display line instruction and 6-bit counter circuit. At the beginning of each LCD frame, the contents of register are copied to the line counter which is increased by CL signal and generates the Line Address for transferring the 132-bit RAM data to the display data latch circuit. However, display data of icons are not scrolled because the MPU can not access Line Address of icons.

COLUMN ADDRESS CIRCUIT

Column Address circuit has an 8-bit preset counter that provides column address to the Display Data RAM as shown in figure 8. When set Column Address MSB / LSB instruction is issued, 8-bit [Y7:Y0] is updated. And, since this address is increased by 1 each a read or write data instruction, microprocessor can access the display data continuously. However, the counter is not increased and locked if a non-existing address above 84H. It is unlocked if a column address is set again by set Column Address MSB / LSB instruction. And the Column Address counter is independent of page address register.

ADC select instruction makes it possible to invert the relationship between the Column Address and the segment outputs. It is necessary to rewrite the display data on built-in RAM after issuing ADC Select instruction. Refer to the following figure 7.

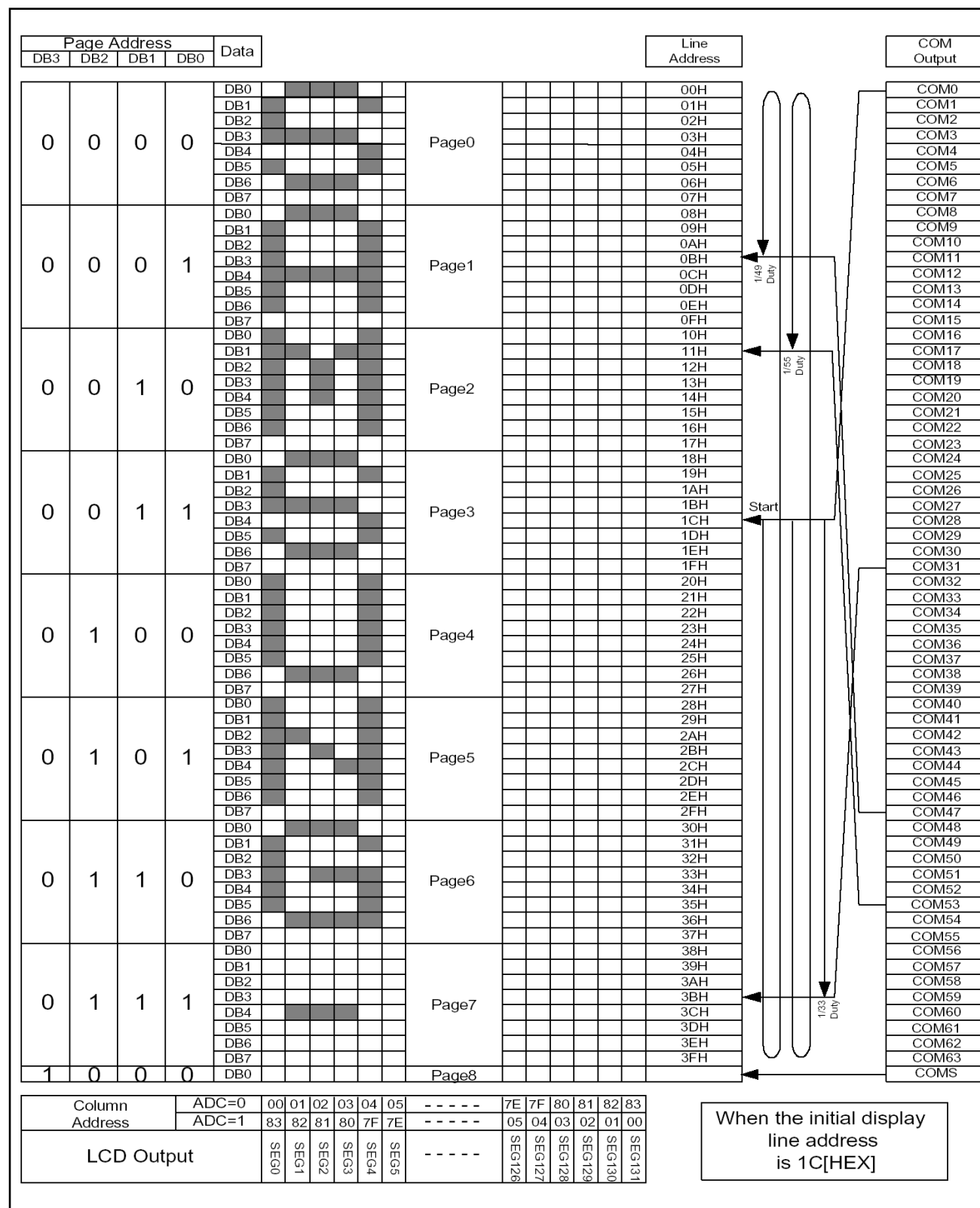
THE RELATIONSHIP BETWEEN THE COLUMN ADDRESS AND THE SEGMENT OUTPUTS

SEG output	SEG 0	SEG 1	SEG 2	SEG 3		SEG 128	SEG 129	SEG 130	SEG 131
Column address [Y7:Y0]	00H	01H	02H	03H		80H	81H	82H	83H
Display data	1	0	1	0		1	1	0	0
LCD panel display (ADC = 0)									
LCD panel display (ADC = 1)									

SEGMENT CONTROL CIRCUIT

This circuit controls the display data by the display ON / OFF, reverse display ON / OFF and entire display ON / OFF instructions without changing the data in the display data RAM.

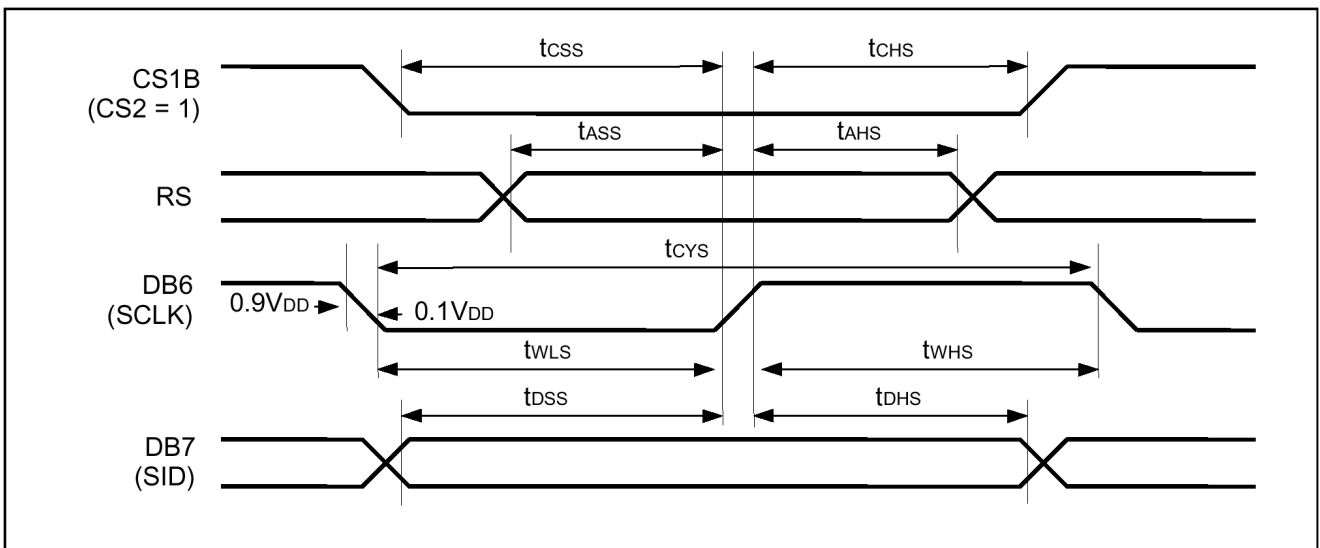
DISPLAY DATA RAM MAP



SERIAL MODE TIMING CHARACTERISTICS

Item	Signal	Symbol	Min.	Typ.	Max.	Unit	Remark
Serial clock cycle	DB6 (SCLK)	tCYS	250	-	-		
SCLK high pulse width		tWHS	100	-	-	ns	
SCLK low pulse width		tWLS	100	-	-		
Address setup time	RS	tASS	150	-	-	ns	
Address hold time		tAHS	150	-	-		
Data setup time	DB7 (SID)	tDSS	100	-	-	ns	
Data hold time		tDHS	100	-	-		
CS1B setup time	CS1B	tCSS	150	-	-	ns	
CS1B hold time		tCHS	150	-	-		

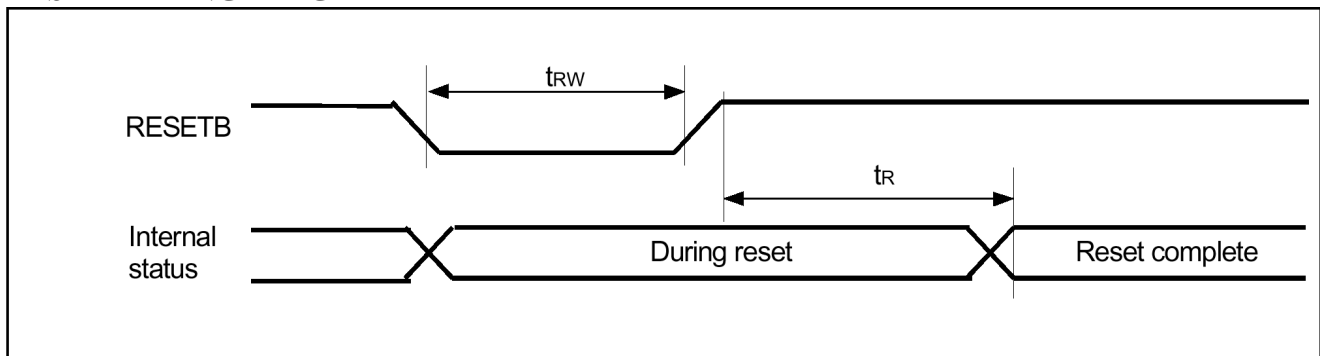
SERIAL INTERFACE CHARACTERISTICS



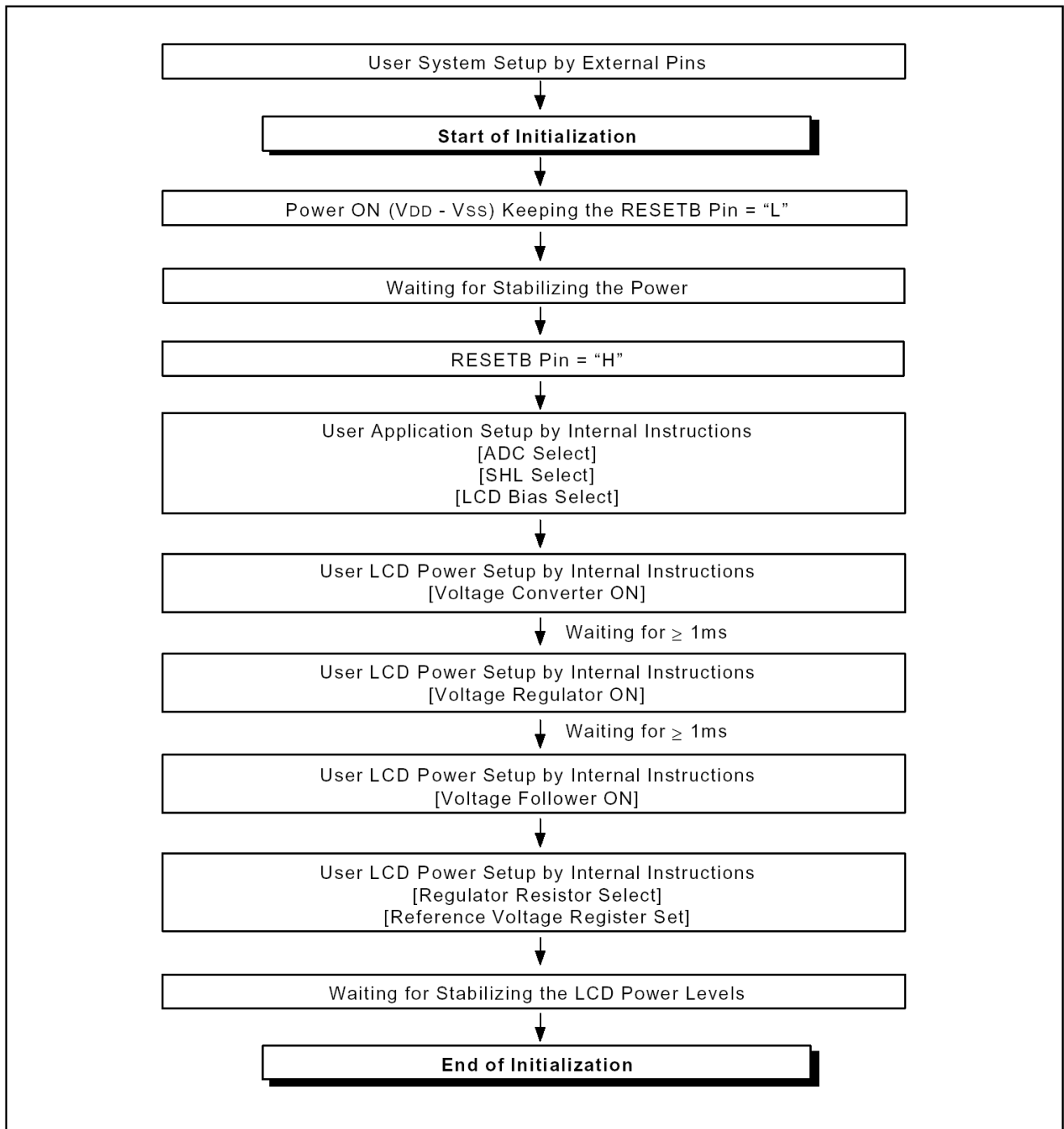
RESET TIMING

Item	Signal	Symbol	Min.	Typ.	Max.	Unit	Remark
Reset low pulse width	RESETB	t _{rw}	1.0	-	-	ns	
Reset time	-	t _r	-	-	1.0	ns	

RESET TIMING DIAGRAM



INITIALIZATION METHOD



ELECTRO-OPTICAL CHARACTERISTICS

MEASURING CONDITION: POWER SUPPLY = V_{OP} / 64 Hz
 TEMPERATURE = 22 ± 5 °C
 RELATIVE HUMIDITY = 60 ± 15 %

ITEM	SYMBOL	UNIT	TYP. STN
RESPONSE TIME	Ton	ms	220
	Toff	ms	280
CONTRAST RATIO	Cr	-	12
VIEWING ANGLE (6 O'clock) Cr ≥ 2	V3:00	°	40
	V6:00	°	70
	V9:00	°	40
	V12:00	°	50

THE ELECTRO-OPTICAL CHARACTERISTICS ARE MEASURED VALUE BUT NOT GUARANTEED ONES.

RELIABILITY OF LCD MODULE

ITEM	TEST CONDITION FOR NORMAL TEMPERATURE	TEST CONDITION FOR WIDE TEMPERATURE	TIME
High temperature operating	50°C	70°C	240 hours
Low temperature operating	0°C	-20°C	240 hours
High temperature storage	60°C	80°C	240 hours
Low temperature storage	-10°C	-30°C	240 hours
Temperature-humidity storage	40°C 90% R.H.	60°C 90% R.H.	96 hours
Temperature cycling	-10°C to 60°C 30 Min Dwell	-30°C to 80°C 30 Min Dwell	5 cycle
Vibration Test at LCM Level	Freq 10-55 Hz Sweep rate: 10-55-10 at 1 min Sweep mode Linear Displacement: 2 mm p-p 1 Hour each for X, Y, Z	Freq 10-55 Hz Sweep rate: 10-55-10 at 1 min Sweep mode Linear Displacement: 2 mm p-p 1 Hour each for X, Y, Z	—

SAMPLING METHOD

SAMPLING PLAN: MIL-STD 105E

CLASS OF AQL: LEVEL II/ SINGLE SAMPLING
 MAJOR-0.65% MINOR – 1.5%

QUALITY STANDARD

DEFECT	CRITERIA	TYPE	FIGURE
SHORT CIRCUIT	-	MAJOR	-
MISSING SEGMENT	-	MAJOR	-
UNEVEN / POOR CONTRAST	-	MAJOR	-
CROSS TALK	-	MAJOR	-
PIN HOLE	$\text{MAX}(a,b) \leq 1/4 W$	MINOR	1
EXCESS SEGMENT	$\text{MAX}(c,d) \leq 1/4 T$	MINOR	1
BUBBLES	$d^* \geq 0.2$ QTY=0	MINOR	2
BLACKS SPOTS	$d \leq 0.3$ N.A.** $0.3 < d \leq 0.4$ QTY ≤ 1 $0.4 < d$ QTY=0	MINOR	2
LINE SCRATCHES	$x \geq 0.7$ $y \geq 0.05$ QTY=0	MINOR	3
BLACK LINE	$x \geq 0.7$ $y \geq 0.05$ QTY=0	MINOR	3

*d = MAX (d₁,d₂)

** N. A . = NOT APPLICABLE

DEFECT TABLE : B

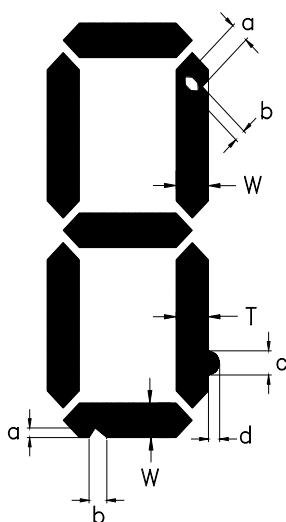
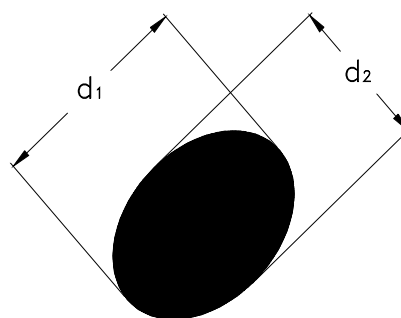
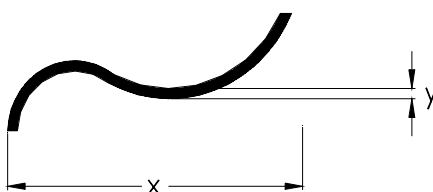


fig . 1



POLARIZER BUBBLES / SPOTS

fig . 2



LINE SCRATCHES / BLACK LINE

fig . 3

QUALITY STANDARD (CONT .)

DEFECT		CRITERIA	TYPE	FIGURE
CHIPS	CONTACT EDGE	$e \leq 1/2T$ $f \leq 1/3W$ $g \leq 3.5$	MINOR	4
	BOTTOM GLASS	$p \leq 1.0$ $q \leq 3.5$ $r \leq 1/2T$		4
	CORNER	$a \leq 1.5$ $b \leq W$		4
	TOP GLASS	$a \leq 3.0$ $b \leq 1/3T$ $c \leq 1/2W$		5
GLASS PROTRUSION		$a \leq 1/4 W$	MINOR	6
RAINBOW		-	MINOR	-

UNLESS STATE OTHERWISE , ALL UNIT ARE IN MILLIMETER .

DEFECT TABLE : B

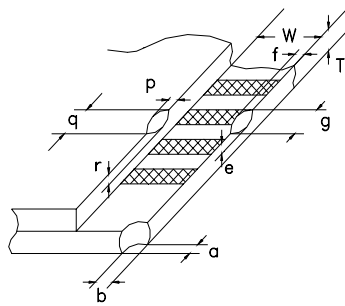


fig . 4

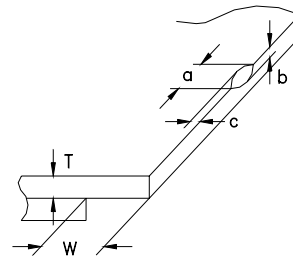


fig . 5

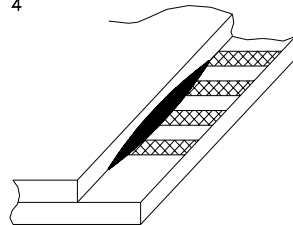


fig . 6

HANDLING PRECAUTIONS

(1) CAUTION OF LCD HANDLING & CLEANING

Use soft cloth with solvent (recommended below) to clean the display surface and wipe lightly.

- Isopropyl alcohol, ethyl alcohol, trichlorotrifluoroethane

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface. Do not use the following solvent;

-water, ketone, aromatics

(2) CAUTION AGAINST STATIC CHARGE

The LCD modules use CMOS LSI drivers, so customers are recommended that any unused input terminal would be connected to V_{DD} or V_{SS} , do not input any signals before power is turned on, and ground your body, work/assembly areas, assembly equipment to protect against static electricity.

Remove the protective film slowly and, if possible, under ESD control device like ion blower and humidity of working room should be kept over 50%RH to reduce risk of static charge.

(3) PACKAGING

Avoid intense shock and falls from a height and do not operate or store them exposed direct to sunshine or high temperature/humidity.

(4) CAUTION FOR OPERATION

It is an indispensable condition to drive LCD's within the specified voltage limit since the higher voltage than the limit causes the shorter LCD life. The use of direct current drive should be avoided because an electrochemical reaction due to direct current causes LCD's undesirable deterioration.

Response time will be extremely delayed at low temperature, and LCD's show dark color at high temperature. However those phenomena do not mean malfunction or out of order with LCD's.

Some font will be abnormally displayed when the display area is pushed hard during operation. But it resumes normal condition after turning off once.

(5) SOLDERING (for Pin type)

It is recommended to complete dip soldering at 270 °C or hand soldering at 280 °C within 3 seconds. The soldering position is at least 3mm apart from the pin head. Wave or reflow soldering are not recommended. Metal pins should not be soldered for more than 3 times and each soldering should be done after cool down of metal pins

(6) SAFETY

For crash damaged or unnecessary LCD's, it is recommended to wash off liquid crystal by either of solvents such as acetone and ethanol and should be burned up later.

When any liquid leaked out of a damaged glass cell comes in contact with your hands, wash it off with soap and water.

WARRANTY

Multi-Inno will replace or repair any of her LCD module in accordance with her LCD specification for a period of one year from date of shipment. The warranty liability of Multi-Inno is limited to repair and/or replacement. Multi-Inno will not be responsible for any subsequent or consequential event.